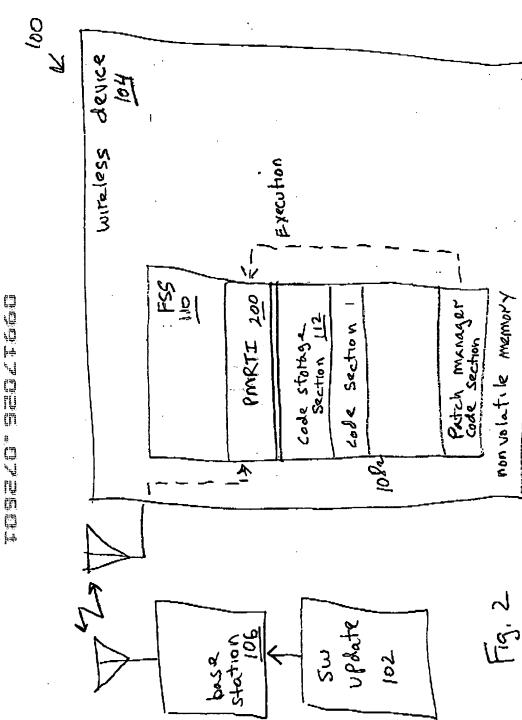
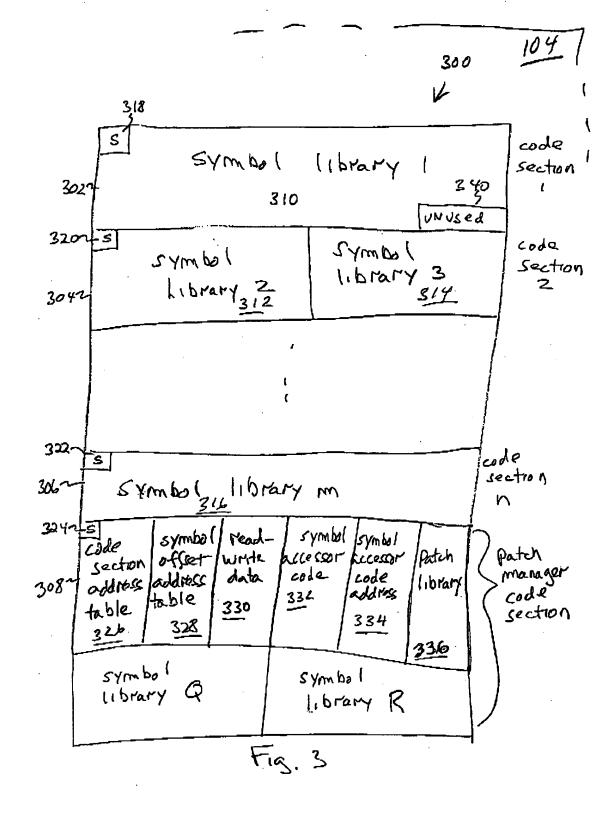


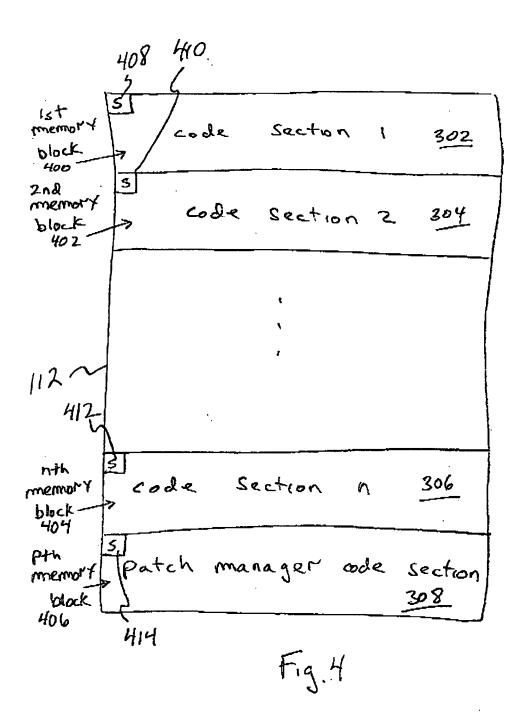
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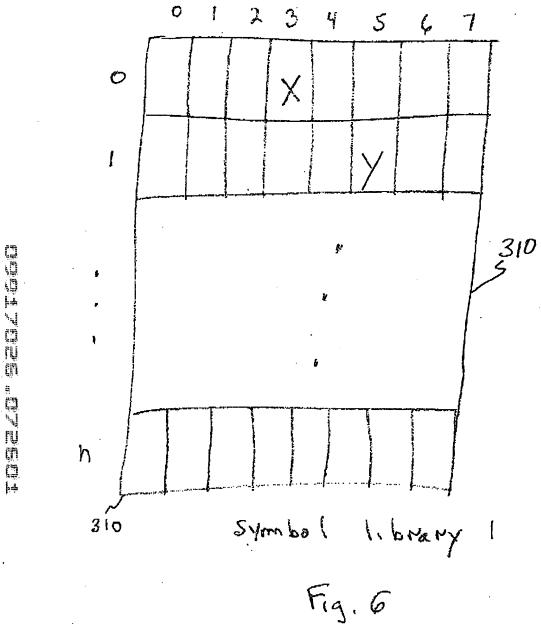


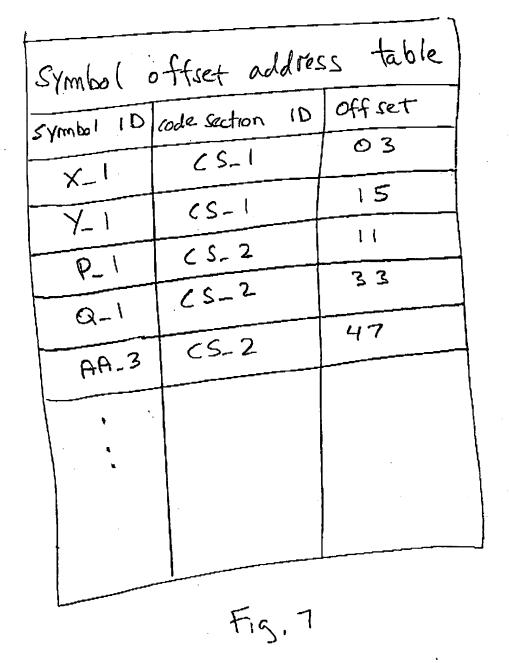




,		The second secon	
32 <i>6</i> ~	code Section Identifi	address table	
	EDGE SCETION LEESTING	7001-3363	
	CS_1	start address 1 (00100)	
	CS-2		
		2 (00200)	
Į			
	<5_h	start address n (00700)	
	PM	start address	
4		(01000)	

Fig. 5





Start GOS 2 into forming system sw SYMP ( arranging symbol libraries into code Sections Storing start of code sections at start addresses 805a / creating contiguously addressed memory 805a2 Identifying memory blocks with code Sections code Sections Storing 80,56 maintaing code section address table 8050 Imaintaining Symbol offset address table 30,52 Storing symbol accessor code address Fig. 8a to Fig. 86

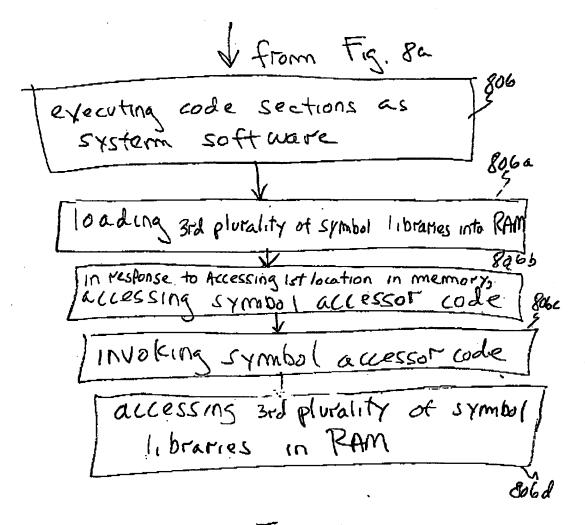


Fig. 86